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[Method and apparatus for shortening display list instructions](#) - all 4 versions »

Q Devic - US Patent 6,072,508, 2000 - [freepatentsonline.com](#)

... the present invention, the partition lookup table 240 comprises ... is addressed by a partition data bit ... Its basic implementation, the look-up table 240 contains ...

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S Palacharla, RE Kessler - Computer Architecture, 1994. Proceedings the 21st Annual ..., 1994 - [ieeexplore.ieee.org](#)

... caches as there is no RAM lookup involved. used fifteen scientific applications, listed in Table 1 from ... The number of instructions executed by each application ...

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[Virtual memory addressing device](#) - all 2 versions »

JA Anderson, RV Van Gelder, LF Yazolilo, JE Braun - US Patent 4,376,297, 1983 - Google Patents

... here that the double-fetch, table lookup address translation ... by bypassing said table look up operation for a ... In combination: auser partition storage register ...

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[Matching partition a linked list and its optimization](#) - all 4 versions »

Y Han - Proceedings of the first annual ACM symposium on Parallel ..., 1989 - [portal.acm.org](#)

... for computing the • 2) matching partition funcaonjl (a ... One may suggest building such an instruction into the ... alternative is to use a lookup table to convert ...

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[Global code motion/global value numbering](#)

C Click - Proceedings of the ACM SIGPLAN 1995 conference on ..., 1995 - [portal.acm.org](#)

... and identical inputs is done with a hash-table lookup. ... equivalent instructions with a single instruction, however the single replacement instruction is not ...

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[\[PDF\] An Instruction Stream Compression Technique](#) - all 5 versions »

P Bird, T Mudge - Electrical Engineering and Computer Science Dept., Univ. of ..., 1996 - [eecs.umich.edu](#)

... Partition the ROM table into sets of sequences. ... Lookup Table ... Our ROM table (containing a compressed tag following a branch instruction), could augment or ...

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[\[CITATION\] VAP—an array processor architecture using cascaded look-up tables](#)

H Keller, A Favre, A Comazzi - Computing Structures for Image Processing, 1983 - Academic Press

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[\[CITATION\] Compact dispatch tables for dynamically typed object oriented languages](#) - all 6 versions »

J Vittek, RN Horspool - Proceedings of the 6th International Conference on Compiler ..., 1996 - Springer

... account a one cycle load latency, a look-up requires TST ... compress dis- patch tables by merging all tables into one ... The goal is to fred a partition of all rows ...

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[\[CITATION\] A PC-Based Logic Simulator Using a Look-Up Table Cascade Emulator](#) - all 6 versions »

H NAKAHARA, T SASAO, M MATSUURA - IEICE TRANSACTIONS on Fundamentals of Electronics, ..., 2006 - IEICE

... A PC-BASED LOGIC SIMULATOR USING A LOOK-UP TABLE CASCADE EMULATOR ... we excluded these data from Table 2. The ... since the LUT cascade emulator partition the given ...

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J Irwin, MD May, HL Muller, D Page - Application-Specific Systems, Architectures and Processors, ..., 2002 -

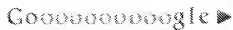
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... and still return using the right partition for a ... the effectiveness of our instruction

partitioning mechanism ... when not optimised to use lookup tables, utilises a ...

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Key authors: [S Palacharla](#) - [R Kessler](#) - [P Ranganathan](#) - [D Tullsen](#) - [L Barroso](#)

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